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Question Booklet Alpha Code





Total Number of Questions: 100 Time: 90 Minutes

Maximum Marks: 100

INSTRUCTIONS TO CANDIDATES

- 1. The Question Paper will be given in the form of a Question Booklet. There will be four versions of Question Booklets with Question Booklet Alpha Code viz. **A, B, C** & **D**.
- 2. The Question Booklet Alpha Code will be printed on the top left margin of the facing sheet of the Question Booklet.
- 3. The Question Booklet Alpha Code allotted to you will be noted in your seating position in the Examination Hall.
- 4. If you get a Question Booklet where the alpha code does not match to the allotted alpha code in the seating position, please draw the attention of the Invigilator IMMEDIATELY.
- 5. The Question Booklet Serial Number is printed on the top right margin of the facing sheet. If your Question Booklet is un-numbered, please get it replaced by new Question Booklet with same alpha code.
- 6. The Question Booklet will be sealed at the middle of the right margin. Candidate should not open the Question Booklet, until the indication is given to start answering.
- 7. Immediately after the commencement of the examination, the candidate should check that the Question Booklet supplied to him/her contains all the 100 questions in serial order. The Question Booklet does not have unprinted or torn or missing pages and if so he/she should bring it to the notice of the Invigilator and get it replaced by a complete booklet with same alpha code. This is most important.
- 8. A blank sheet of paper is attached to the Question Booklet. This may be used for rough work.
- 9. Please read carefully all the instructions on the reverse of the Answer Sheet before marking your answers.
- 10. Each question is provided with four choices (A), (B), (C) and (D) having one correct answer. Choose the correct answer and darken the bubble corresponding to the question number using Blue or Black Ball Point Pen in the OMR Answer Sheet.
- 11. Each correct answer carries 1 mark and for each wrong answer 1/3 mark will be deducted. No negative mark for unattended questions.
- 12. No candidate will be allowed to leave the examination hall till the end of the session and without handing over his/her Answer Sheet to the Invigilator. Candidates should ensure that the Invigilator has verified all the entries in the Register Number Coding Sheet and that the Invigilator has affixed his/her signature in the space provided.
- 13. Strict compliance of instructions is essential. Any malpractice or attempt to commit any kind of malpractice in the Examination will result in the disqualification of the candidate.

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1.	Which of the following A) -	g special symbol can B) #	be ι C)		ole na D)	
2.	At a time a function c A) one value C) three values	an return	,	two values many values		
3.	What will be the outp void main() { char str1[10] ="hello", printf("%d",strcmp(st	.str2[10]="hello";	tate	ements?		
	} A) 1 C) -1		B) D)	0 none of these		
4.	Which of the following A) dot(.) operator C) * operator	g operator is used to a	B)	ess structure elen & operator none of the abov		3?
5.	If there is any error w A) 0	hile opening a file, for B) 1		() returns EOF	D)	NULL
6.	What will be the result A) 7	It of the following post		expression evalua 24	ation D)	
7.	In a queue data struct A) front end C) both A) and B)	ture new element is ii	B)	rted at rear end none of the abov	⁄e	
8.	Which of the following A) single linked list C) both A) and B)	g is a one way list?	,	double linked list		
9.	A) non terminal node C) leaf node B) internal node C) leaf node C) leaf node C) leaf node					
10.	Which data structure A) queue C) linked list	is used for backtracki	B)	in DFS traversal ? stack none of the abov		

11.	The default visibility I A) public C) private	abel of a class in C++	,	protected none of the above	e
12.	Which of the following A) ()	g operator that cannot B) []		overloaded?	D) ?:
13.	A derived class with s A) single inheritance C) multilevel inherita		B)	ed multiple inheritand hierarchical inher	
14.	In C++ run time polyr A) virtual functions C) function overloadi	morphism is achieved	B)	ng operator overload data abstraction	ling
15.	How many types of te	emplates are in C++ ? B) 3	C)	4	D) 2
16.	Java compiler transla A) object code C) machine code	ates source code into	,	byte code none of these	
17.	Which of the following A) interfaces C) interface	g keyword is used to c	B)	ne interfaces in Jav interfase Interface	va?
18.	AWT in Java stands (A) Application Windows (C) Advanced Windows	ow Toolkit	,	Abstract Windowi Abstract Window	•
19.	Which of the following A) Container C) Component	g is the basic class for	B)	SWING GUI comp Jcomponent All of the above	oonents?
20.	Oracle Call Interface A) Type 1 JDBC driv C) Type 3 JDBC driv	er	,	Type 2 JDBC driv	

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21.	Which are the properties of transaction in c i) Atomicity ii) Consistency A) i, ii and iii only C) ii only	latabase ? iii) Isolation iv) Concurrency B) All the above D) ii and iii only
22.	Another name of tuple is A) Record C) File	B) Field D) Relation
23.	In an ER diagram, symbol used for an attrib A) Rectangle C) Double Rectangle	oute is B) Ellipse D) Diamond
24.	In three tier architecture, query processing A) Application tier C) Database tier	languages resides in B) User tier D) None of these
25.	Suppose you are assigned with the task of without deleting table definition. Which stat A) DROP TABLE Student C) UPDATE TABLE Student	•
26.	A stored program needs to be invoked autoinserted into a table. Then which of the followal Define a row level trigger C) Define a procedure	•
27.	A telephone company wishes to give an off all customers with a plan "PLAN197". Then A) MODIFY Customers SET ValidPrd = ValidPrd+10 WHERE Plan = "PLAN197"	
	B) ALTER table Customers SET ValidPrd = ValidPrd+10 WHERE Plan = "PLAN197"	
	C) SELECT Customers SET ValidPrd = ValidPrd+10 WHERE Plan = "PLAN197"	
	D) UPDATE Customers SET ValidPrd = ValidPrd+10 WHERE Plan = "PLAN197"	

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28.	Normalization is a method to remove which A) Insert anomaly C) Update anomaly	n of the following anomaly ? B) Deletion anomaly D) All the above	
29.	Which of the following is a DDL STATEME A) GRANT C) ALTER	NT ? B) COMMIT D) UPDATE	
30.	"Shared Memory, Shared disk, Shared not that have been proposed for A) Mobile databases C) OODBMS	hing" are the three main architectures B) Parallel databases D) Distributed DBMS	
31.	 One of the following is not true in case of CMS A) Allow users to place content on a website B) Allows website content to be accessible in a single machine only C) Allow users to control, manage and structure the content in real-time D) Allow users to do updates, deletions and alterations of Web content 		
32.	 Identify correct CSS rule to set all elements on the page center-aligned, with a red text color. A) p { text-align: center; color: red;} B) p { text-align center; color red;} C) p { text-align: center; color: red } D) p { text-align: center, color: red;} 		
33.	Which is the correct syntax to call an external JavaScript file in the current HTML document ? A) <script src="jsfile.js"></script> B) <script href=" jsfile.js"></script> C) <import src=" jsfile.js"></import> D) <script link=" jsfile.js"></script>		
34.	Statement which is wrong about HTML DC A) W3C (World Wide Web Consortium) sta B) A standard for accessing documents C) Constructed as a linked list of Objects D) When a web page is loaded, the brows the page	andard	

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35.	Write function to delete a cookie named "u A) delcookie("user", "", time()+3600); B) delcookie("user", "", time()-3600); C) setcookie("user", "", time()+3600); D) setcookie("user", "", time()-3600);	ser"
36.	Which of the following is not an example for A) https://doi.org/10.2016/j.jup.1	or container tag ? C) D) <center></center>
37.	All communications between browsers and A) HTTP B) FTP	d servers use protocol. C) HTML D) SMTP
38.	Information sent from a form with this meth limits on the amount of information to send A) GET C) ECHO()	
39.	In PHP which type of array can be used to student? A) Numeric array C) Associative array	assign Register Number to Name of B) Multi dimensional array D) All of these
40.	In the HTML < input> tag which type attributext input but it masks the character as social. A) TextArea B) Text	
41.	Which fundamental component of Android user when Battery is low? A) View C) Activities	is used to receive the notification to the B) Intent D) Broadcast Receiver
42.	Dalvik Virtual Machine (DVM) is the part of Android operating system. A) Application frame work C) Android Runtime	B) Linux Kernel D) Libraries
43.	Which Android SQLite Java Class is used database query? A) SQLiteDatabase C) Content Values	to provides access to the results of a B) SQLiteOpenHelper D) Cursor

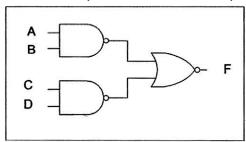
44.	In which directory An A) /assets	droidManifest.xml laye B) /src		files are stored ? /res/values	D) /res/layout
45.	Which of the following A) Dalvik converter C) Mobile Interpretive	g converts Java byte o	B)	e into Dalvik byte o Dex compiler Java Virtual Macl	
46.	Which method is use something new? A) Context.startActiv C) Context.sendBroad	rity()	В)	or get an existing Context.startServ All the above	
47.	You can create a cus A) android.widget.Vic C) android.view.View	ew	B)	ass android.widget.Li android.content	inearLayout
48.	 Write a code to send SMS to another AVD with number 9946671122 A) SmsManager sms=new SmsManager(); sms.sendTextMessage("9946671122", null, "Have a nice day", null, null); B) SmsManager sms=new SmsManager(); sendSMS("9946671122", null, "Have a nice day", null, null); C) SmsManager sms=SmsManager.getDefault(); sms.sendTextMessage("9946671122", null, "Have a nice day", null, null); D) SmsManager sms=SmsManager.getDefault(); sms.sendTextMessage("Have a nice day", null, "9946671122", null, null); 				
49.	What type of CSS is <h1 style="color:blue">h1 style="color:blue">h1 style="color:blue">h</h1>	the following code sni ue; ">A Blue Headin	g B)		/e
50.	What is true about Content Providers? A) They dictate the UI and handle the user interaction to the smart phone screen B) They handle background processing associated with an application C) They handle communication between Android OS and applications D) They handle data and database management issues				
51.	Convert the hexadec A) (42,687) ₁₀ C) (46,687) ₁₀	imal number (B65F) ₁₆	B)	its equivalent deci (36,687) ₁₀ (32,687) ₁₀	imal value.

- 52. Simplify the Boolean expressions $\overline{X}YZ + XZ$ to a minimum number of literals.
 - A) Z(X + Y)

B) $\overline{Z}(X + Y)$

C) X(Z + Y)

- D) $X(\overline{Z} + Y)$
- 53. Simplify the Boolean functions $F(X, Y, Z) = \sum (0, 2, 4, 5, 6)$
 - A) $Z + X\overline{Y}$
- B) $\overline{Z} + X\overline{Y}$
- C) $\overline{Z} + XY$
- D) $\overline{Z} + \overline{XY}$
- 54. Write the equivalent Boolean expression for the following logic circuit.



A) $F = A + \overline{BC} + D$

 $\mathsf{B}) \;\; \mathsf{F} = \overline{\mathsf{A}} + \overline{\mathsf{B}} + \overline{\mathsf{C}} + \overline{\mathsf{D}}$

C) $F = AB\overline{CD}$

- D) F = ABCD
- 55. The OR operation can be produced with
 - A) two NOR gates

B) three NAND gates

C) four NAND gates

- D) both answers A) and B)
- 56. All Boolean expressions can be implemented with
 - A) NAND gates only or NOR gates only
 - B) combinations of NAND and NOR gates
 - C) combinations of AND gates, OR gates, and inverters
 - D) None of the above
- 57. Which of the following codes exhibit even parity?
 - A) 10011000

B) 01111010

C) 11010111

- D) 11010101
- 58. To enter a byte of data serially into an 8-bit shift register, there must be
 - A) one clock pulse

B) eight clock pulses

C) two clock pulses

- D) four clock pulses
- 59. A modulus-12 counter must have
 - A) 4 flip-flops

B) 12 flip-flops

C) 3 flip-flops

D) synchronous clocking

60.	A 5-bit binary co	ounter has	a maximum	modulus of
	A) 3			B) 8
	C) 32			D) 16

61. Calculate the average CPI when a program with the instruction mix and the CPI for each instruction type given below is executed on a uniprocessor.

Instruction Type	СРІ	Instruction Mix
Arithmetic and logic	1	60%
Load/store with cache hit	2	10%
Branch	4	10%
Memory reference with cache miss	8	10%

A) 2	B) 3
C) 3.5	D) 2.5

62. Calculate MIPS rate in case when a benchmark program results in the execution of 2 million instructions on a 380-MHz processor with an average CPI of 2.5.

A) 120	B) 250
C) 152	D) 160

63. Calculate the effective CPI for the machine having clock rate of 200 MHz that gives the following measurement for a set of benchmark programs.

Instruction Type	Instruction Count (millions)	Cycles Per Instruction
Arithmetic and logic	10	1
Load/store with cache hit	8	2
Branch	2	4
Others	4	3

	<u> </u>
A) 1.22	B) 1.92
C) 5.24	D) 322

64. Consider a 32-bit microprocessor, with a 16-bit external data bus, driven by an 8-MHz input clock. Assume that this microprocessor has a bus cycle whose minimum duration equals four input clock cycles. What is the maximum data transfer rate across the bus that this microprocessor can sustain, in bytes/s?

A)	4 Mbytes/sec.	B) 3 Mbytes/sec.
C)	2 Mbytes/sec.	D) 1.5 Mbytes/sec.

65. A computer has a cache, main memory, and a disk used for virtual memory. What is the average time in nanoseconds required to access a referenced word on this system? Using the case provided in table below:

Location of referenced word	Probability	Total time for access in ns
In cache	0.9	20
Not in cache but in main memory	0.06	80
Not in cache or in main memory	0.04	12000

A) 251	B) 400.2
C) 350	D) 502.8

66. Consider a machine with a byte addressable main memory of 2¹⁶ bytes and block size of 8 bytes. Assume that a direct, mapped cache consisting of 32 lines is used with this machine. How is a 16-bit memory address divided into tag, line number, and byte number?

A)	(8, 4, 7)	B)	(6, 4,	8)
C)	(6, 2, 7)	D)	(8, 5,	3)

67. What is the disk capacity of a magnetic disk drive with 8 surfaces, 512 tracks per surface, and 64 sectors per track with sector size 1 KB?

A) 512 KB B) 64 KB C) 256 MB D) 128 MB

68. Consider a 32-bit microprocessor having 32-bit instructions composed of two fields, the first byte contains the opcode and the remainder the immediate operand or an operand address. What is the maximum directly addressable memory capacity (in bytes)?

A) 16 MBytes

B) 8 MBytes

C) 32 MBytes

D) 64 MBytes

69. The number of 3 to 8 line decoders to construct a 9 to 512 line decoder with an enable input and without using any other logic gates are

A) 56 B) 73 C) 69 D) 91

70. A given microprocessor has words of 1 byte. What is the smallest and largest integer that can be represented in the Ones complement representations?

A) 0; 255 B) -127;127 C) 0, 99 D) None of the above

	Match the following: I) SNMP II) NFS III) DHCP IV) POP	a) Host Conb) Mail Tranc) Network Id) Name Sye) File Shari	sfer Managemer stem	nt		
	A) I-c, II-e, III-a, IV-b		B)	I-c, II-e, III-b, IV	-a	
	C) I-d, II-c, III-e, IV-a		D)	I-b, II-d, III-a, IV	-е	
72.	Snort is a A) Network Operating C) Security Auditing S	•	•	Intrusion Detection System		ystem
73.	Bandwidth for a signa d is 0.		·			
	A) 2 MHz	B) 4 MHz	C)	6 MHz	D)	8 MHz
74.	In AVR microcontrolle A) there is a carry fro B) there is a carry fro C) the result of an op D) a carry occurred in	m D3 to D4 m D7 eration is too	large	tion		
75.	A topology that is resp components that make		•	e geometric arra	ıngem	ent of
	A) Complex	B) Physical	C)	Logical	D)	Incremental
76.	The access control po allow the entity, by its resources. A) Discretionary access C) Role based access	own volition,	to enable a		access ess co	ntrol
	,		,			
77.	In 8086 microprocess address accessed by A) 66210H	•	n MOV CX,			000H, the
78.	The minimum and ma A) 46 and 1500 C) 32 and 1518	,	length of IE B)			

79.	The ADC of AVR has A) 8	bit	resoluti C)		D)	16
80.	An organization is graaddress, last address A) 205.16.37.32, 205 B) 205.16.37.39, 205 C) 205.16.37.32, 205 D) 205.16.37.32, 205	anted address bloc and number of ad 3.16.37.47, 16 3.16.37.47, 64 5.16.37.255, 32	, k 205.1	16.37.39/28, n=28	,	
81.	The type of firewall the about TCP connection A) Packet filter C) Proxy	•	B)	and keeps track of Stateful inspection Message digest		formation
82.	Shell script command A) c = \$num1 * \$num C) c= `expr \$num1 *	12	B)	num1 and num2. c= `expr num1 * c= `expr num1 * r		
83.	Timer 0 acts as a course. A) the CS02-CS00 at B) the CS02-CS00 at C) the COM00-COM0 D) the WGM01:WGM	re 110 or 111 re 000 01 are 00				
84.	At the sender side the method (segment size A) 1 1 0 1 1 1 1 1 1 1 1 B) 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	e=4 bits) is used, tl 0 1 1 0 1 1				
85.	Computer Security tri A) Confidentiality, Int B) Confidentiality, Int C) Accountability, Au D) Non-repudiation, A	egrity, Authenticity egrity, Availability thenticity, Availabil	lity			

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86.	Multistreaming and multihoming are allowed A) TCP C) IP	B)	UDP SCTP	Protocol.
87.	The register is responsible for handling all			rdware interrupts in AVR
	A) TIMSK	B)	GICR	
	C) MCUCR	D)	IVCE	
88.	Linux command to display the owner of the	e pro	cesses alo	ng with the processes.
	A) ps ax	B)	ps ag	
	C) ps aux	D)	ps dx	
89.	A computer program that can to run independent of itself to other hosts on a network, target system. A) Worm	typica B)	ally through	•
	C) Backdoor	D)	Spyware	
90.	Using RSA algorithm, find the value of put $q = 11$ and $d = 7$.	blic ke	ey, if the pla	ain text $M = 5$ and $p = 3$,
	A) (3, 33) B) (7, 33)	C)	(3, 20)	D) (5, 33)
91.	LDS instruction loads data from memory lo	ocatio	on to the sp	ecified register and
	A) CS register	B)	SS register	r
	C) DS register	D)	ES register	r
92.	HDLC contains system manager	ment i	information	l .
	A) I Frame	B)	S Frame	
	C) A Frame	D)	U Frame	
93.	The hashed passwords are stored separate	tely fr	om the use	er IDs in a file known as
	A) Hashed password file	В)	Symbol tab	ole file
	C) Shadow password file	D) .	Authentica	tion file
94.	In a fibre optic cable, the signal is propaga	ated a	long the in	ner core by
	A) Modulation		Dispersion	-
	C) Refraction	•	Reflection	

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95.	AVR Instruction CBI PORTB,1 A) Clear the PORT B register B) Clear the first bit of PORT B register C) Set the PORT B register D) Set the first bit of PORT B register	
96.	The nonmaskable interrupt in 8086 A) INTR C) Trap	B) Breakpoint D) Overflow
97.	Attack the ability of a network server to resoverflowing the tables used to manage such. A) CON spoofing attack B) SYN flooding attack C) Masquerade attack D) SYN spoofing attack	
98.	An analog signal has a bit rate of 8000bps data elements are carried by each signal eA) 8 bits/baud C) 128 bits/baud	-
99.	The Bus Interface Unit of 8086 microproce store in A) Register C) Queue	ssor prefetches the instruction and B) Memory D) Cache
100.	 The instruction, MOV AX,[BX] is an examp A) Register addressing mode B) Register direct addressing mode C) Register relative addressing mode D) Register indirect addressing mode 	le of

Space for Rough Work

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