## DETAILEDSYLLABUS FOR THE POST OF Maintenance Engineer (Electronics) in Kerala State Film Development Corporation Limited.

(Cat.No. : 129/2020)

(Total Marks-100)

MODULE - MARKS	CONTENTS
I (15 Marks)	<ul> <li>Electronic Devices <ul> <li>Energy bands in intrinsic and extrinsic</li> <li>Semiconductors</li> <li>Carrier transport:</li> <li>Diffusion current, drift current, generation and recombination of carriers, Poisson and continuity equations. P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell.</li> </ul> </li> </ul>
II (20 Marks)	<ul> <li>Analog Circuits</li> <li>Diode circuits: Clipping, clamping and rectifiers.</li> <li>BJT and MOSFET amplifiers: Biasing, small signal analysis, frequency response. Current mirrors and differential amplifiers.</li> <li>Op-amp circuits: Amplifiers, differentiators, integrators, active filters, Schmitt triggers and oscillators.</li> </ul>
III (20 Marks)	<ul> <li>Digital Circuits <ul> <li>Number representations:</li> <li>Binary, integer and floating-point- numbers.</li> <li>Combinatorial circuits:</li> <li>Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and CMOS implementations, arithmetic circuits, multiplexers, decoders.</li> <li>Sequential circuits:</li> <li>Latches and flip-flops, counters, shift-registers, finite state machine.</li> <li>Data converters:</li> <li>Sample and hold circuits, ADCs and DACs.</li> <li>Semiconductor memories:</li> </ul> </li> </ul>

	<ul> <li>ROM, SRAM, DRAM.</li> <li>Computer organization: Machine instructions and addressing modes, ALU, pipelining.</li> </ul>
IV (15 Marks)	<ul> <li>Communications</li> <li>Analog communications: Amplitude modulation and demodulation, angle modulation and demodulation, superheterodyne receivers.</li> <li>Information theory: Entropy, channel capacity theorem.</li> <li>Digital communications: PCM, DPCM, digital modulation schemes (ASK, PSK, FSK, QAM, Hamming codes.</li> <li>Computer communication</li> </ul>
V (15 Marks)	<ul> <li>ANALOG INTEGRATED CIRCUITS         <ul> <li>Differential amplifier configurations, Operational amplifiers, Active filters, Monolithic Voltage Regulators, Data converters.</li> </ul> </li> </ul>
VI (15 Marks)	<ul> <li>Power Electronics</li> <li>Power Devices : Characteristics and control of MOSFET, IGBT, IGCT Switches . Power dissipation in switches</li> <li>Zener voltage regulators – DC – DC Converters – Buck &amp; Boost Converters – Switched Mode Power Supply (SMPS) – Voltage Source inverters – Pulse width modulation for control of convrters – DC Motor Speed Control - Induction Meter Speed Control – Stepper motor drives.</li> </ul>

**NOTE:** - It may be noted that apart from the topics detailed above, questions from other topics prescribed for the educational qualification of the post may also appear in the question paper. There is no undertaking that all the topics above may be covered in the question paper